

SPECIFICATION FOR TFT MODULE

MODULE NO.: IPS101A106A

CUSTOMER NO.:

Rev No.: B

GTK	PREPARED BY	CHECKED BY	APPROVED BY	
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DATE	2020.07.02	2020.07.02	2020.07.02	

	SIGNATURE	DATE
CUSTOMER APPROVAL		

Notes:

- 1. Please contact GTK before assigning your product based on this module specification.
- 2. To improve the quality of product, and this product specification is subject to change without any notice.

REVISION RECORD

Rev No.	Rev date	Contents	Remarks
0	2019-06-11	First release	Preliminary
Α	2019-11-25	Update the BL driver circuit diagram,and CIE	Page 5 & 6
В	2020-07-02	Change Contrast ratio and NTSC	Page 6

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1. GENERAL INFORMATION

No.	Item	Contents	Unit
1	LCD size	10.1 inch (Diagonal)	/
2	Display mode	Normally Black/Transmissive	/
3	Viewing direction(eye)	FREE	/
4	Gray scale inversion direction	-	/
5	Resolution(H*V)	1200 *1920 Pixels	/
6	Module size (L*W*H)	227.38*142.32*2.50	mm
7	Active area (L*W)	216.576*135.36	mm
8	Pixel pitch (L*W)	0.1128*0.1128	mm
9	Interface type	MIPI interface	1
10	Color Depth	16.7M	/
11	Module power consumption	2.6	W
12	Back light type	LED	1
13	Driver IC	HX8279-D OR COMPATIBLE	/
14	Weight	TBD	G

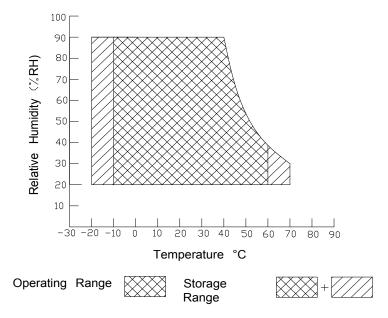
2. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Power supply input voltage for TFT	VDD	-0.3	3.6	V	
Backlight current (normal temp.)	ILED	-	100	mA	
Operation temperature	Тор	-10	+60	°C	Note1
Storage temperature	Tst	-20	+70	°C	Note1
Humidity	RH	20%	90%	RH	Note1

Note1:

1). The relative humidity and temperature range are as below sketch, 90%RH Max.

2). The maximum wet bulb temperature $\leq 40^{\circ}$ C and without dewing.



3. ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS(at Ta=25°C)

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Power supply input voltage	VDD	1.7	1.8	2.0	V	
I/O logic voltage	VDDIO	-	-	-	V	
Input voltage 'H' level	VIH	2.7	-	3.3	V	
Input voltage 'L' level	VIL	0	-	0.5	V	
	IVDD	-	41	-	mA	
Power Supply Current	VSP		60	-	mA	
	VSN		15	-	mA	
Dower Supply Voltage	VSP	4.5	5.4	6.0	V	
Power Supply Voltage	VSN	-4.5	-5.4	-6.0	V	

4. BACKLIGHT CHARACTERISTICS

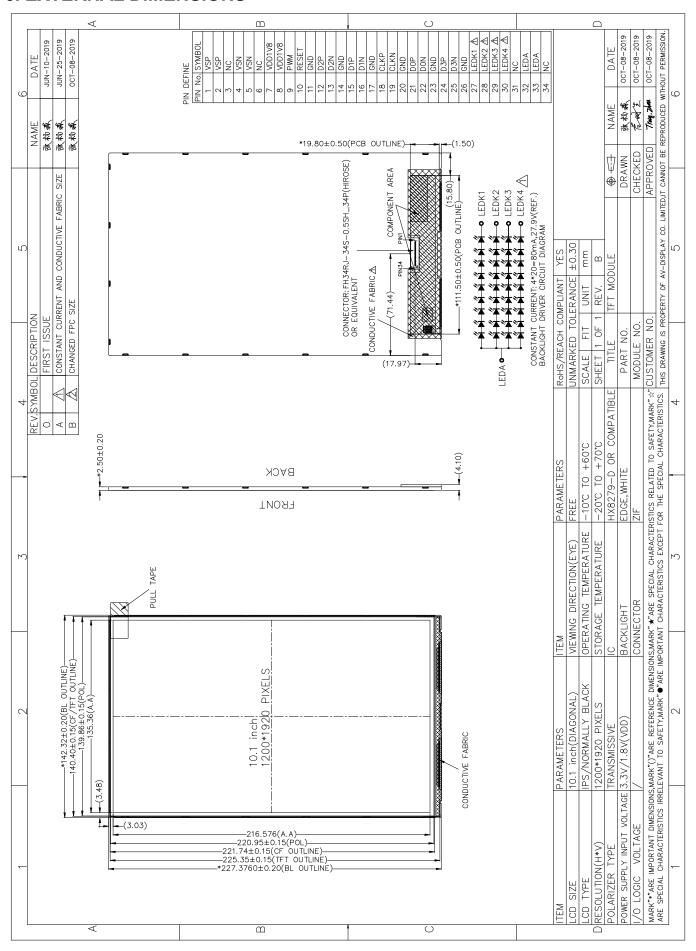
(at Ta=25°C,RH=60%)

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Item	Symbol	Min.	Тур.	Max.	Unit	Note	
LED forward voltage	VF	-	27.9	-	V		
LED forward current	IF	-	80	-	mA	IF=20*4mA	
LED power consumption	PLED	-	2.232	-	W	Note1	
Number of LED	-		12		PCS		
Connection mode	-	9 in series 4 in parallel			1		
LED life-time	-	20000	-	-	Hrs	Note2	

Note1 : Calculator value for reference : IF*VF = PLED

Note2: The LED life-time define as the estimated time to 50% degradation of initial brightness at Ta=25°C and IF =80mA. The LED lifetime could be decreased if operating IF is larger than 80mA.

5. EXTERNAL DIMENSIONS



6. ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response time	Tr+ Tf		-	25	-	ms	FIG.1	Note 1
Contrast ratio	Cr	-	800	1000	-	-	FIG.2	Note 2
Surface luminance	Lv	θ=0°	240	300	-	cd/m ²	FIG.2	Note 3
Luminance uniformity	Yu	θ=0°	75	80	-	%	FIG.2	Note 4
NTSC	-	θ=0°	60	72	-	%	FIG.2	Note 5
		∅=90°	80	85	-	deg	FIG.3	
Viouring angle	θ	∅=270°	80	85	-	deg	FIG.3	Note 6
Viewing angle		∅=0°	80	85	-	deg	FIG.3	Note 6
		∅=180°	80	85	-	deg	FIG.3	
	Red x			0.63		-		
	Red y			0.35		-		
	Green x			0.32		-]	
CIE (x,y)	Green y	θ=0°	Тур	0.60	Тур	-	FIG.2	Note F
chromaticity	Blue x	Ø=0° Ta=25°C	-0.04	0.15	+0.04	-	CIE1931	Note 5
	Blue y	10-25 0	14-23 0	0.06		-		
	White x			0.30		-		
	White y			0.34		-		

Note1. Definition of response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For additional information see FIG1.

Note2. Definition of contrast ratio

Contrast ratio(Cr) is defined mathematically by the following formula.

For more information see FIG.2.

Contrast ratio= Luminance measured when LCD on the "White" state Luminance measured when LCD on the "Black" state

Measured at the center area of the LCD

Note3. Definition of surface luminance

Surface luminance is the luminance with all pixels displaying white.

For more information see FIG.2.

Lv = Average Surface Luminance with all white pixels(P1,P2,P3,,Pn)

Note4.Definition of luminance uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

Yu = Minimum surface luminance with all white pixels (P1,P2,P3,.....,Pn)
Maximum surface luminance with all white pixels (P1,P2,P3,.....,Pn)

Note5. Definition of color chromaticity (CIE1931)

CIE (x,y) chromaticity,The x,y value is determined by screen active area center position P5.For more information see FIG.2.

Note6. Definition of viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10. angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.3.

For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope or DMS series Instruments or compatible. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-5or BM-7 photo detector or compatible.

FIG.1. The definition of response Time

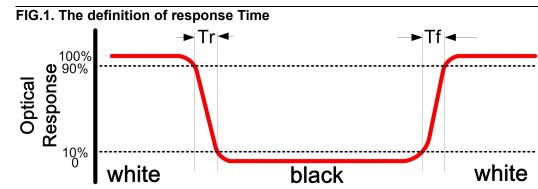


FIG.2. Measuring method for contrast ratio, surface luminance, luminance uniformity, CIE (x,y) chromaticity

H,V: Active area

Light spot size \varnothing =5mm(BM-5) or \varnothing =7.7mm (BM-7)50cm distance or compatible distance from the LCM surface to detector lens.

Test spot position : see Figure a.

measurement instrument: TOPCON's luminance meter BM-5 or BM-7 or compatible, see Figure b.

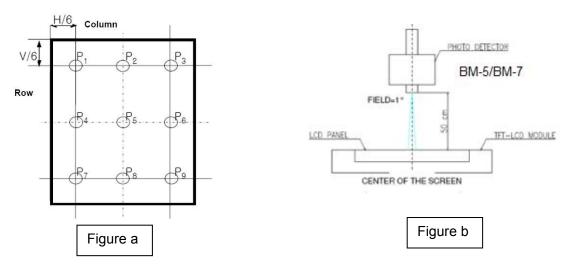
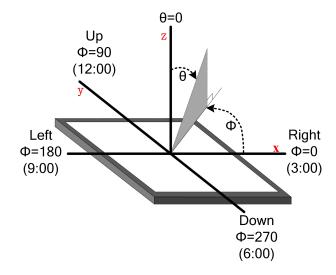


FIG.3. The definition of viewing angle



7. INTERFACE DESCRIPTION

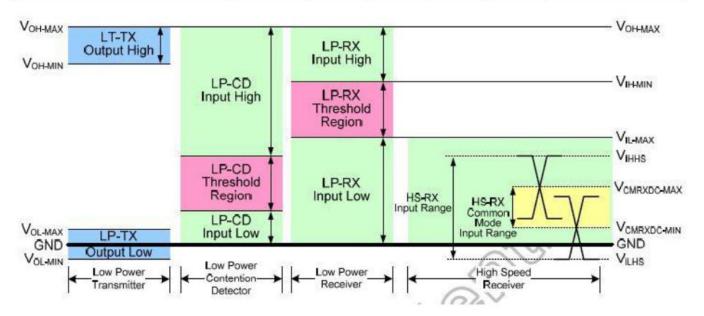
Module Interface description

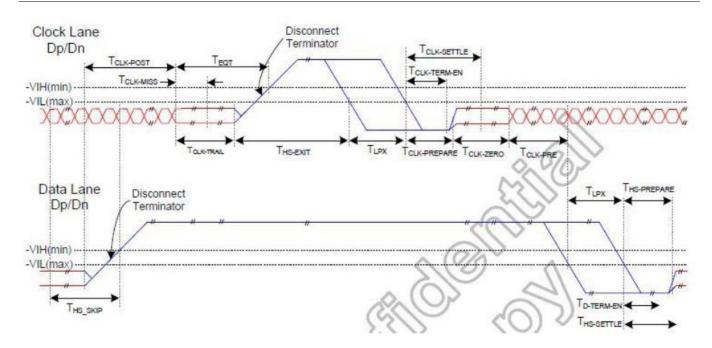
Interface No.	Name	I/O or connect to	Description
1-2	VSP	I	Power Supply 5.4V
3	NC	-	Not Connected
4-5	VSN	I	Power Supply -5.4V
6	NC	-	Not Connected
7-8	VDD1V8	I	Power Supply 1.8V
9	PWM	0	PWMOUT
10	RESET	I	LCM RESET
11	GND	Р	Ground
12	D2P	Р	MIPI Differential Data Input
13	D2N	Р	MIPI Differential Data Input
14	GND	Р	Ground
15	D1P	Р	MIPI Differential Data Input
16	D1N	Р	MIPI Differential Data Input
17	GND	Р	Ground
18	CLKP	Р	MIPI Differential Clock Input
19	CLKN	Р	MIPI Differential Clock Input
20	GND	Р	Ground
21	D0P	Р	MIPI Differential Data Input
22	D0N	Р	MIPI Differential Data Input
23	GND	Р	Ground
24	D3P	Р	MIPI Differential Data Input
25	D3N	Р	MIPI Differential Data Input
26	GND	Р	Ground
27	LB1	I	LED Cathode(-)
28	LB2	I	LED Cathode(-)
29	LB3	I	LED Cathode(-)
30	LB4	I	LED Cathode(-)
31	NC	-	Not Connected
32-33	VLED	I	LED Anode(+)
34	NC	-	Not Connected

8.AC CHARACTERISTICS

Module AC CHARACTERISTICS

Parameter	Symbol	Min	Тур	Max	Unit	Condition	
MIPI digital operation current	I _{VCCIF}	=	08	H3	mA		
MIPI digital stand-by current	I _{VCCIFST}	2	(92)	20	uA		
MIPI Characteristics for High Speed Receiver							
Single-ended input low voltage	V _{ILHS}	-40	0 5 0	733	mV		
Single-ended input high voltage	V _{IHHS}	=	-	460	mV		
Common-mode voltage	V_{CMRXDC}	70	1773	330	mV		
Differential input impedance	Z _{ID}	80	100	125	Ω		
HS transmit differential voltage($V_{OD}=V_{DP}-V_{DN}$)	V _{OD}	140	200	270	mV		
MIPI Characteristics for Low F	ower Recei	ver					
Pad signal voltage range	V _I	=	8. 7. 8	=:	mV		
Ground shift	V_{GNDSH}	<u>=</u>	(' =')	1400	mV		
Output low level	V _{OL}	-50	927	50	mV		
Output high level	V _{OH}	1.1	1.2	1.3	V		





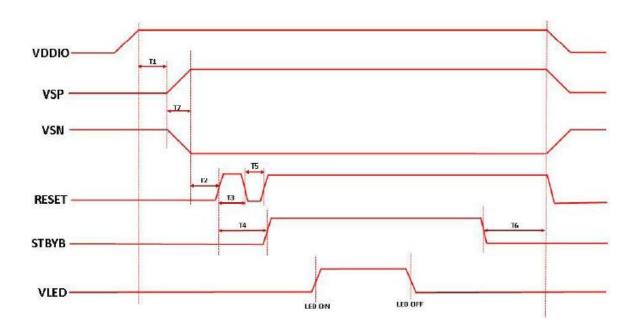
< Switching the clock lane between clock transmission and low-power mode >

	It	em	Symbol	min	typ	max	UNIT
LCD		Frame Rate	(2)	2	60	max 2047 - 2047 995	Hz
LCD		Pixels Rate	878		156	170	MHz
	DCLV	Frequency	fCLK	2	468	- - - 2047 - - - 2047	MHz
	DCLK	Period	Tclk	-	2.14		ns
		Horizontal total time	tHP	- 60 - 156 - 468 2.14 1340 2047 1200 - 24 - 80 60 1944 2047 1920 - 2 - 10 - 14 14 -	t _{CLK}		
	:	Horizontal Active time	tHadr	1200			t _{clk}
.	Horizontal	Horizontal Pulse Width	tHsync	a l	24	874	t _{CLK}
		Horizontal Back Porch	tHBP	æ	80	548	t _{CLK}
Timing		Horizontal Front Porch	tHFP	a)	60	6 - 8 - 4 - 10 2047 00 1 - 0 - 14 2047 00 - 14 2047 00 - 1 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0 - 0	t _{clk}
		Vertical total time	tvp	60 -	t _H		
		Vertical Active time	tVadr		1920	- - - 2047 - - - 2047	t _H
	Vertical	Vertical Pulse Width	tVsync	22	2	828	t _H
		Vertical Back Porch	tVBP	-	10	1=1	t _H
		Vertical Front Porch	tVFP	2	14	828	t _H
	Bit	Rate	TX SPD (Mbps)	980	980	995	Mbps
Lane				ā	4	170	Lane

9. POWER SEQUENCE

To prevent the device damage from latch up and Improve subjective display effect, the power ON/OFF sequence shown below must be followed.

Module POWER SEQUENCE



Parameter -	Value			11.3
	Min.	Тур.	Max.	Units
T1	10			(ms)
T2	10			(ms)
T3	disce.	5		(ms)
T4	10			(ms)
T5	0.3	0.5	1	(ms)
T6	100			(ms)
T7	0.1	1	10	(ms)
Т8				(ms)

10. RELIABILITY TEST CONDITIONS

No.	Test item	Test condition		Inspection after test
11.1	High temperature storage test +70°C/240 hours			
11.2	Low temperature storage test	-20°C/240 hours		Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Current changing value before test and after test is 50% larger; 2. Function defect: Non-display,abnormal-d isplay,missing lines, Short lines,ITO
11.3	High temperature operating test	+60°C/120 hours		
11.4	Low temperature operating test -10°C/120 hours			
11.5	Temperature cycle storage test	rature cycle storage test		
11.6	High temperature high humidity test	+50°C*90% RH/120 hours		
11.7	Vibration test	Frequency : 250 r/min Amplitude : 1 inch Time: 45min		
11.8 Drop t		Drop direction: 1 corner/3 edges/6 sides 10 times		
		Packing weight(kg)	Drop height(cm)	corrosion;
	Drop test	<11	80±1.6	3.Visual defect : Air bubble in the LCD,Seal
		11≦G<21	60±1.2	leak,Glass crack.
		21 ≦ G<31	50±1.0	
		31 ≦ G<40	40±0.8	
11.9	ESD test	Air discharge: ±8KV, 10times Contact discharge: ±4KV, 10times		

Remark:

- 1. The test samples should be applied to only one test item.
- 2. Sample size for each test item is 3~5pcs.
- 3. For High temperature high humidity test, Pure water(Resistance>10M Ω) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.B/L evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence B/L has.
- 6. Failure judgment criterion: Basic specification, Electrical characteristic, Mechanical characteristic, Optical characteristic.
- 7. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

11.INSPECTION CRITERION

Refer to 《Inspection Criterion for outsourcing TFT -To customer》 V1.0,DOCUMENT NO.: GTK(WI)-00-QA-016

12. HANDLING PRECAUTIONS

12.1 Mounting method

The LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly:

- .lsopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- .Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated :

- Soldering flux
- •.Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

12.4 Packing

Module employ LCD elements and must be treated as such.

- Avoid intense shock and falls from a height.
- •. To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

12.5 Caution for operation

- •.It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- •.An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- •.Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- •.If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- •.A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
- •. Usage under the maximum operating temperature, 50%Rh or less is required.
- •. When fixed patterns are displayed for a long time, remnant image is likely to occur.

12.6 Storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- •.Storing in an ambient temperature 10°C to 30°C, and in a relative humidity of 45% to 75%. Don't expose to sunlight or fluorescent light.
- •. Storing in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- •. Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature

range

•. Storing with no touch on polarizer surface by the anything else.

It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

12.7 Safety

- •.It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- •.When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

13. PRECAUTION FOR USE

- **13.1** A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- **13.2** On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.
- •. When a question is arisen in this specification.
- •. When a new problem is arisen which is not specified in this specifications.
- •. When an inspection specifications change or operating condition change in customer is reported to GTK, and some problem is arisen in this specification due to the change.
- •. When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. PACKING SPECIFICATION

Please consult our technical department for detail information.

15. INITIALIZATION CODE

TBD

16. HSF COMPLIANCE

•.This products complies with ROHS 2011/65/EU and 2015/863/EU \ REACH 1907/2006/EC requirements, and the packaging complies with 94-62-EC.